



EXPEDITED PROCEDURE - EXAMINING GROUP 2818

09/808,750

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Huy Thanh Vo	Examiner:	Son Mai
Serial No.:	09/808,750	Group Art Unit:	2818
Filed:	March 15, 2001	Docket:	303.723US1
Title:	DEVICE AND METHOD TO REDUCE WORDLINE RC TIME CONSTANT IN SEMICONDUCTOR MEMORY DEVICES		

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SUPPLEMENTAL AMENDMENT & RESPONSE UNDER 1.116

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Washington, D.C. 20231

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IN THE CLAIMS

Please substitute the claim set in the appendix entitled Clean Version of Pending Claims for the previously pending claim set. The substitute claim set is intended to reflect amendment of previously pending claims 1, 5, 8, 15, 19, 26, 30, 37, 42, 45, and 49. The specific amendments to individual claims are detailed in the following marked up set of claims.

1. (Twice amended) A memory array, comprising:
 - a number of memory cells having a first source/drain region and a second source/drain region and a gate region;
 - a number of source lines coupled to the first source/drain region of at least one memory cell;
 - a number of bit lines coupled to the second source/drain region of at least one memory cell;
 - a number of wordlines coupled to the gate region of at least one memory cell;
 - a strapping line of lower resistance than the wordlines coupled to a single wordline wherein the strapping line bypasses a portion of the single wordline, and wherein the strapping line is spaced apart from adjacent conductive structures by a distance greater than a wordline pitch[; and], and wherein a width of the strapping line is greater than a width of the wordlines; and